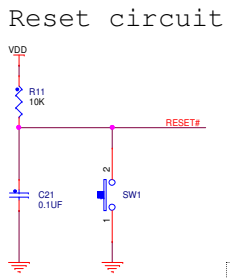
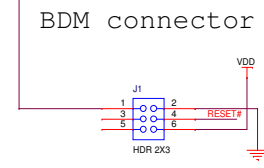
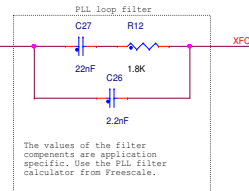


Decoupling capacitor only. Do not connect to power supply voltage and do not connect external load.

Decoupling capacitor only. Do not connect to power supply voltage and do not connect external load.

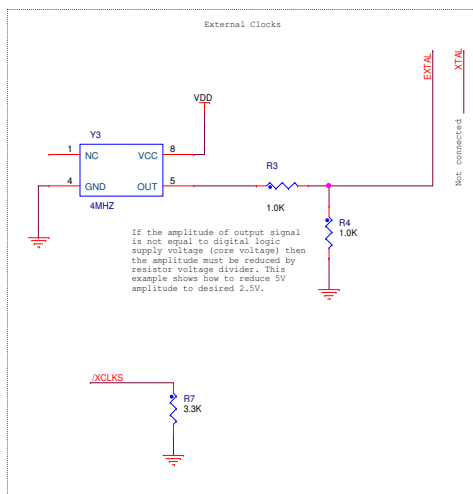
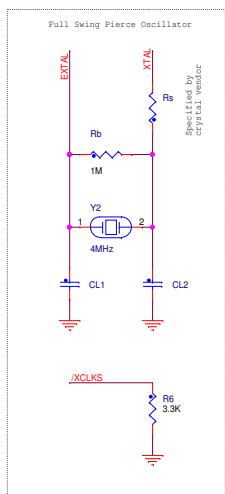
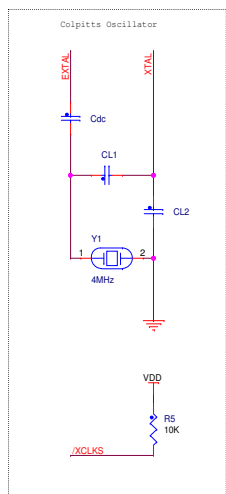


Pins MODA (PE5), MODB (PE6) and MODC (BRGD) selects the mode of MCU.



The values of the filter components are application specific. Use the PLL filter calculator from Freescale.

### Oscillator options



If the amplitude of output signal is not equal to digital logic supply voltage (core voltage) then the amplitude must be reduced by resistor voltage divider. This example shows how to reduce 5V amplitude to desired 2.5V.

		<b>Freescale Semiconductor RCSC</b> 1. maja 1009 765 61 Roznov p. R. Czech republic, Europe		
		This document contains information proprietary to Freescale Semiconductor and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of Freescale Semiconductor.		
Designer: <Designer>	Drawing Title: <Title>	ICAP Classification: FCP	FIUC: X	PUB:
Drawn by: <DrawBy>	Page Title: <PageTitle>			
Approved: <Approver>	Size C	Document Number SCH-XXXXX PDF: SPF-XXXXX	Rev X	
Date: Monday, April 23, 2012		Sheet 1 of 1		