

# Layerscape<sup>®</sup> 1088A and 1048A Processor

# LS1088A

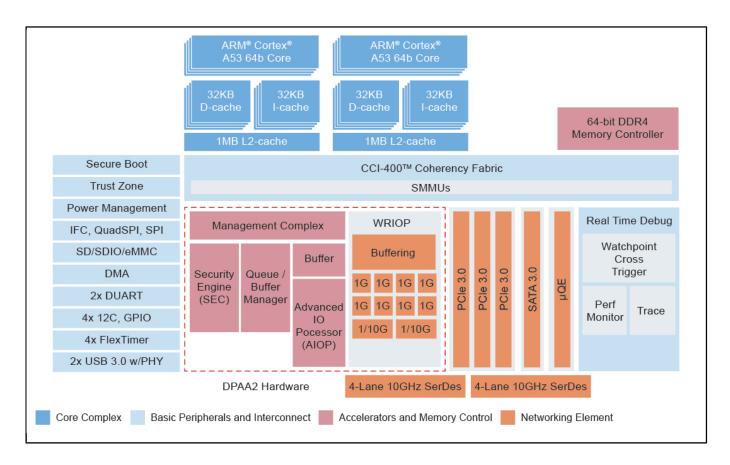
Last Updated: Apr 11, 2024

The LS10x8A family of multicore communications processors combines four up to and eight Arm<sup>®</sup> Cortex<sup>®</sup>-A53 cores with the advanced, high-performance data path and network peripheral interfaces required for wireless access points, networking infrastructure, intelligent edge access, including virtual customer premise equipment (vCPE) and high-performance industrial applications.

The Arm<sup>®</sup> general-purpose processors and DPAA2 are supported by a powerful software toolkit that provides a higher level of hardware abstraction and makes software development quick and simple. This combination balances ease-of-use with high-performance processing in a Linux<sup>®</sup> environment that is familiar to any software programmer.

Layerscape processors are part of NXP's EdgeVerse<sup>™</sup> edge computing platform.

## LS1088A Block Diagram Block Diagram



### View additional information for Layerscape® 1088A and 1048A Processor.

Note: The information on this document is subject to change without notice.

#### www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.