

Correction to ADS5121 Schematic: FuseBox Write Power Supply

Implementation Board: ADS5121
Revision Number: Rev4

Initial schematics for the ADS5121 Evaluation Board showed AVDD_FUSEWR connected to 3.3 V. These schematics have now been updated with AVDD_FUSEWR as a no connect.

Applying power to AVDD_FUSEWR before the other MPC5121e power supply domains are applied and stable can cause fuses in both the user and reserved sections of the IIM/Fuse Block Module to be inadvertently blown.

This can cause the MBX block in the MPC5121e to become unusable. Date code, product revision information, and certain other calibration information can also be corrupted.

This problem occurs only when AVDD_FUSEWR is applied before or at the same time as the other MPC5121e power supplies are applied or when AVDD_FUSEWR is removed at the same time or after the other MPC5121e power supplies are shut down.

A future modification on the ADS5121 ADS board will connect AVDD_FUSEWR to ground. With this pin as a no connect or with this pin connected to ground, it will not be possible to write the fuses inadvertently.

In user designs that need to modify the user fuse block, power to AVDD_FUSEWR must not be applied until all other MPC5121e power pins are fully powered. Power to AVDD_FUSEWR must be removed before any of the other MPC5121e power pins are turned off.

The latest schematics of ADS5121 that include this correction can be found at:
<http://www.silicontkx.com/support/downloads.php>

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